



THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: William John Saiki et al.

Serial No.: 10/044,273

Group Art Unit: 2643

Filed: January 10, 2002

Examiner: Not Yet Assigned

Title: **HIGH VOLTAGE GENERATION AND REGULATION SYSTEM FOR DIGITAL MULTILEVEL NONVOLATILE MEMORY**

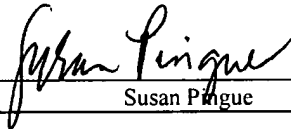
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Susan Pique

PRELIMINARY AMENDMENT

Commissioner for Patents
Washington, DC. 20231

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Sir:

Prior to examination of the above-identified application, please replace the application as filed with the enclosed substitute specification.

REMARKS

Enclosed is a substitute specification and a marked up version of the substitute specification showing the changes made as listed below.

In paragraph 0030, a grammar correction was made;

In paragraph 0042, the enable (ENB) signal 28 is corrected;

In paragraphs 0052, 0058, reference numbers were corrected.

In paragraphs 0064 and 0065, signal names have been corrected.

No new matter has been added.